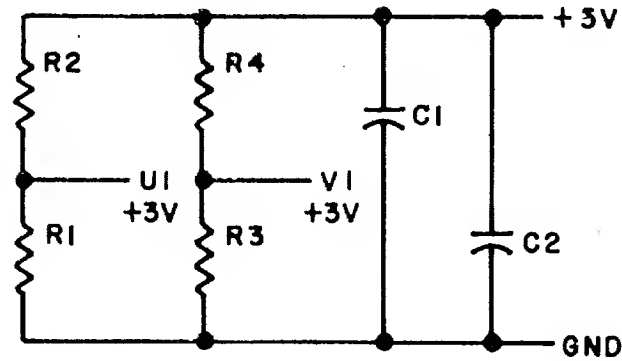
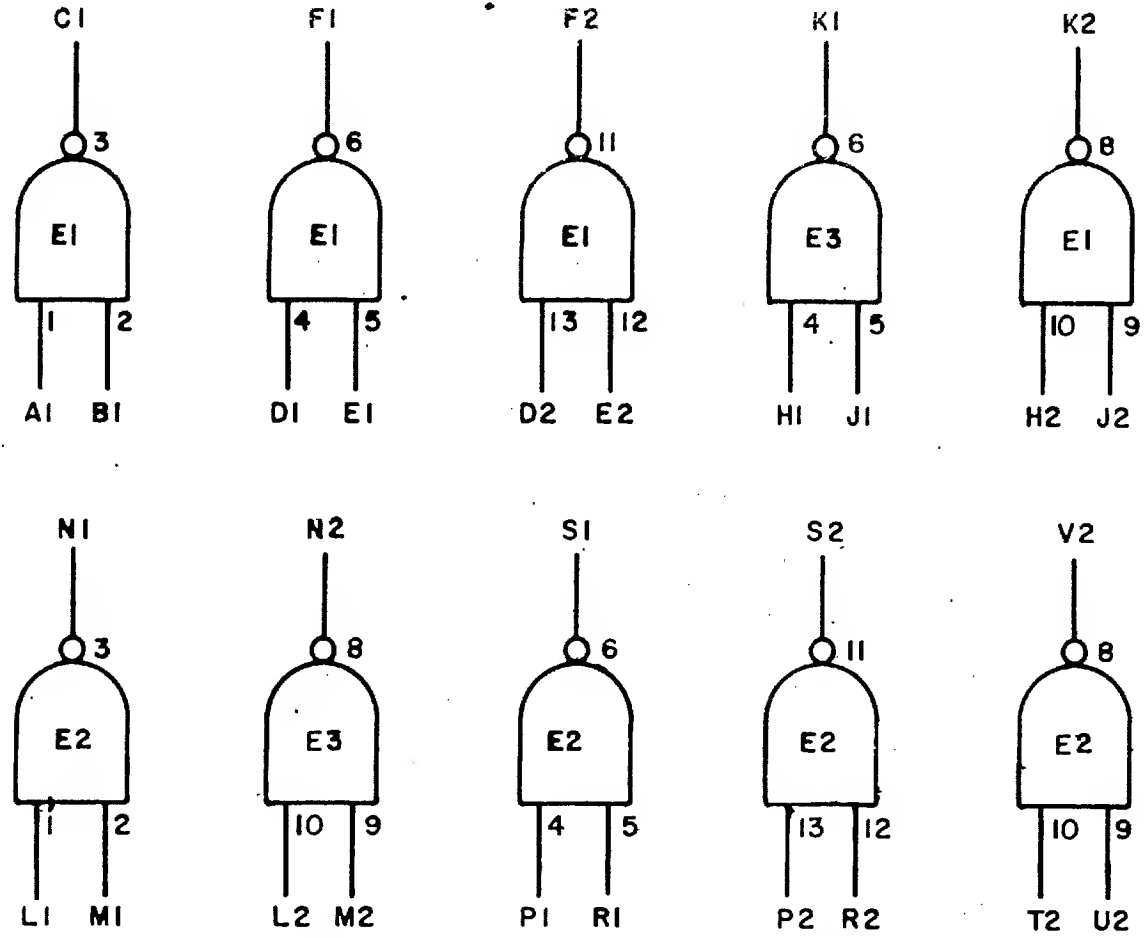


THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY. COPYRIGHT 1967 BY DIGITAL EQUIPMENT CORPORATION

+5V ——— A2  
NOT USED -15V ——— B2  
GND ——— C2, T1



NOTES:  
PIN 7 ON EACH IC = GND  
PIN 14 ON EACH IC = +5V

E1 THRU E3	INTEGRATED CKT. DEC7400N	1905575
R1 AND R3	RES. 750 1/4W 5% CC	1301401
R2 AND R4	RES. 330 1/4W 10% CC	1300293
C1 AND C2	CAP. .01MFD 100V 20% DISC	1001610
PARTS LIST		A-PL-M113-0-0
REFERENCE DESIGNATION	DESCRIPTION	PART NO.

REVISIONS	DRN. <i>M. Haller</i>	DATE <i>3-15-67</i>	TRANSISTOR & DIODE CONVERSION CHART	<b>digital</b> EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE 10-2 INPUT NAND GATES M113.	SIZE B	CODE CS	NUMBER M113-0-1	REV. C
CHK CHG NO. REV. A	CHK'D <i>M. Sullivan</i>	DATE <i>4-21-67</i>							
6425	ENG. <i>L. White</i>	DATE <i>4-21-67</i>							
6801 B	PROD.	DATE							
00001 C									

PINK

DIST. 324/434/435